

**News Release**

**Date: Aug 29 2015**

## Emprog announces support for the TI MSP432™ Cortex M4F.

**Silicon Valley, CA – Aug 29, 2015** – [Emprog](#) announces complete support for the TI MSP432 Cortex M4F family of Devices. The MSP432P401R LaunchPad enables you to develop high performance applications that benefit from low power operation. It features the MSP432P401R a 48MHz ARM Cortex M4F, 95uA/MHz active power and 850nA RTC operation, 14-bit 1MSPS differential SAR ADC and AES256 accelerator.

Emprog ThunderBench is a complete C/C++ development tool, offering customers all that they need to complete their Cortex-M based application. It comes with an optimized C/C++ compiler, IDE, debugger, linker, libraries, and many ready-to-use BSPs for the ARM family of devices. ThunderBench has a wide extended ecosystem of Debug JTAG tools such as SEGGER JLINK and several middleware/stack options for any embedded need.

Finally, embedded developers are no longer limited in their choice of tools for TI. Emprog added full support for the TI MSP432 Cortex M4F to its already existing support of the TI MSP430. Although both devices have different core architectures, but it is quite helpful to know that both devices are supported by the same ONE solution

For more info: [www.emprog.com/ti](http://www.emprog.com/ti)

Or watch this MSP432 Launchpad video: [http://youtu.be/sD\\_0owAVgWQ](http://youtu.be/sD_0owAVgWQ)

### About Emprog

Emprog is a provider of enhanced integration of standard development tools for embedded programming. The company's flagship product ThunderBench has the best deployment of any Eclipse IDE platform. ThunderBench for ARM enables thousands of engineers to develop their products using high performance 32-bit Cortex/ARM devices in several industry areas such as medical, consumer electronics, automotive, wireless, LED, and telecom. A wide range of partners and middleware vendors are supporting ThunderBench via Eclipse plugin capabilities. For more information visit [www.emprog.com](http://www.emprog.com)

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**Emprog Contact:**

Dan Augustine, Marketing Contact

Email: [info@emprog.com](mailto:info@emprog.com)

Tel: 650-241-4050

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